1417 FOUR-TERMINAL CAPACITANCE STANDARD

User and Service Manual

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WARRANTY

We warrant that this product is free from defects in material and workmanship and, when properly used, will perform in accordance with applicable IET specifications. If within one year after original shipment, it is found not to meet this standard, it will be repaired or, at the option of IET, replaced at no charge when returned to IET. Changes in this product not approved by IET or application of voltages or currents greater than those allowed by the specifications shall void this warranty. IET shall not be liable for any indirect, special, or consequential damages, even if notice has been given to the possibility of such damages.

THIS WARRANTY IS IN LIEU OF ALL OTHER WARRANTIES, EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED TO, ANY IMPLIED WARRANTY OF MERCHANTABILITY OR FITNESS FOR ANY PARTICULAR PURPOSE.
WARNING

OBSERVE ALL SAFETY RULES
WHEN WORKING WITH HIGH VOLTAGES OR LINE VOLTAGES.

Dangerous voltages may be present inside this instrument. Do not open the case
Refer servicing to qualified personnel

HIGH VOLTAGES MAY BE PRESENT AT THE TERMINALS OF THIS INSTRUMENT

WHENEVER HAZARDOUS VOLTAGES (> 45 V) ARE USED, TAKE ALL MEASURES TO
AVOID ACCIDENTAL CONTACT WITH ANY LIVE COMPONENTS.

USE MAXIMUM INSULATION AND MINIMIZE THE USE OF BARE
CONDUCTORS WHEN USING THIS INSTRUMENT.

Use extreme caution when working with bare conductors or bus bars.

WHEN WORKING WITH HIGH VOLTAGES, POST WARNING SIGNS AND
KEEP UNREQUIRED PERSONNEL SAFELY AWAY.

CAUTION

DO NOT APPLY ANY VOLTAGES OR CURRENTS TO THE TERMINALS OF THIS
INSTRUMENT IN EXCESS OF THE MAXIMUM LIMITS INDICATED ON
THE FRONT PANEL OR THE OPERATING GUIDE LABEL.
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<table>
<thead>
<tr>
<th>Capacitance Value</th>
<th>Ratio Accuracy</th>
<th>D Accuracy</th>
<th>Approximate Terminal Impedance</th>
<th>E Max* (Vrms)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>100 &amp; 120 Hz</td>
<td>1 kHz</td>
<td>100 &amp; 120 Hz</td>
<td>ZA (Ω)</td>
</tr>
<tr>
<td>(Internal Standard)</td>
<td></td>
<td></td>
<td>ZB (Ω)</td>
<td></td>
</tr>
<tr>
<td>1 µF</td>
<td>0.02%</td>
<td>0.04%</td>
<td>±0.001</td>
<td>±0.001</td>
</tr>
<tr>
<td>10 µF</td>
<td>0.02%</td>
<td>0.04%</td>
<td>±0.001</td>
<td>±0.001</td>
</tr>
<tr>
<td>100 µF</td>
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<td>0.06%</td>
<td>±0.001</td>
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<td>0.06%</td>
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<td>0.2%</td>
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<td>±0.003</td>
<td>±0.001</td>
<td>±0.1</td>
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<tr>
<td>1 F</td>
<td>0.25%</td>
<td>±0.01</td>
<td>±0.001</td>
<td>±0.003</td>
</tr>
</tbody>
</table>

Capacitance:
Internal Standard: 1 µF in 7-switch-selected decade values
External Standard: Indicated capacitance, multiplied by C ext/µF.

Capacitance Accuracy, Direct-Reading: 0.25% plus ratio accuracy at 100 Hz, 120 Hz and 1 kHz, 20 to 25°C, with low applied voltage (< ¼ Emax) using internal standard and a proper four-terminal measurement. (May also be used as a two-terminal standard, with a D < 1 and a capacitance change from the four-terminal value of < 0.5% up to 1 mF at 120 Hz or less).

Capacitance Ratio: Accuracy see table above.

Dissipation Factor: 0.01 at 100 Hz, 120 Hz and 1 kHz. For D accuracy, see table.

Terminal Impedance: See figure and table (approximate values given).

Temperature Coefficient: Approximately -140ppm/°C.

Voltage Characteristic: Approximately +0.3 % change from 0 V to E max (see table) at 100 Hz. Less at higher frequencies.

Mechanical: Bench cabinet

Dimensions: 14.7 cm H x 21.5 cm W x 13.2 cm D (5.9" x 8.5" x 5.25").

Weight: 2.7 kg (6 lb.) net, 5 kg (11 lb.) shipping.

Order Number
1417-9700 Four-Terminal Capacitance Standard
Chapter 1

INTRODUCTION

1.1 Purpose

The 1417 Four-Terminal Capacitance Standard is a standard used to calibrate bridges and meters that measure high values of capacitance with a 4-terminal connection. It also has some use in the calibration of instruments that make a 2-terminal connection. Most instruments that measure high capacitance do so at 100 Hz, 120 Hz or 1 kHz; the 1417 is compensated to have good direct-reading accuracy at these frequencies. It can be calibrated to be highly precise, making use of its precise capacitance-scaling ability.

1.2 Description

The 1417 uses 2 precision transformers to multiply the value of a capacitance standard by factors of 10 up to 106. It has an internal standard of 1 μF so that 7 decade values of 1 μF to 1 F are switch selected. Other values may be obtained by use of an external standard.

This standard does not act as a capacitor at dc and the application of dc can alter the capacitance values.

CAUTION
Do not apply dc.

The IET 1417 Four-Terminal Capacitance Standard is a precision capacitance-scaling device, and not intended as a precision capacitance standard, but rather as a calculated standard. It uses two precision ratio transformers to scale a 1 mF capacitor to values up to 1 farad; therefore the accuracy of these high values depends on the ratio accuracy of the transformers and the accuracy of the 1 mF calibration, with the latter being more important as long as the ratio remains constant.

The 1 mF polystyrene capacitor used in the 1417 is not a highly stable standard. It has a relatively large temperature coefficient, with a value also somewhat dependent upon the inductance of the transformers, which varies with voltage level. With signal levels held below 1/4 Emax, the specified direct-reading accuracy can be expected to hold for all values, with best capacitance accuracy obtained by multiplying the 1kHz, 1 mF value by the specified nominal ratio accuracy.

Calibration

The 1417 is not calibrated in the same manner as other standards.
A typical calibration might consist of:

- Measure and record the 1 mF values at all three frequencies (at <1/4 Emax) to make sure they are within specified tolerance.

- Measure all values at the lowest frequency and calculate ratio errors (primarily to make sure there are no opens, shorts, or serious damage to the transformer dividers).

Because of measuring capabilities, the actual measured values (other than 1mF) are intended as a functional check only, since nominal ratios multiplied by the 1mF calibrated value will still yield the more reliable results.

- Measure and record dissipation factor for all values.

Figure 1-1. IET 1417 Standard
1.3 Controls and Connections

There are 2 panel knobs driving rotary switches. The CAPACITANCE switch selects the capacitance value as indicated when the internal standard is used. When an external standard is used, the value is that indicated by the C dial, multiplied by $C_{ext}/1 \mu F$ (see Para 2.6).

The TEST FREQUENCY switch selects proper padding capacitors and resistors to give good direct-reading capacitance accuracy and a D of .01 at 100 Hz, 120 Hz and 1 kHz (see Para 2.7 for use at other frequencies). It also has a fourth position that removes the internal 1 $\mu F$ standard and connects instead the EXTERNAL STANDARD terminals (see Para 2.6).

There are 4 pairs of binding posts on standard 19-mm (3/4-in.) spacing plus 2 ground (case) binding posts. All pairs have terminals marked H and L, and L terminals are all common.

The pairs of terminals on the left are those that should be connected to the measuring instrument. They are labeled POTENTIAL AND CURRENT, common nomenclature for a 4-terminal standard. Generally, these are connected to bridge terminals with similar markings.

The VOLTmeter terminals on the right are directly across the standard capacitor used, whether it is the internal standard or an external standard. Their primary use is to monitor the voltage across the standard so that corrections can be made when precise measurements are required (see paragraph 2.4). These terminals may also be used to add capacitance to the internal standard (see paragraph 2.6).

The EXTERNAL STANDARD terminals are used to connect an external capacitor to obtain other than decade values (see paragraph 2.6). These terminals are switched in the circuit by the TEST FREQUENCY switch.
Chapter 2

OPERATION

2.1 General

The manner in which the 1417 should be used depends on many factors: the accuracy required, the test frequency, the applied voltage, the capacitance values required, and the instrument with which it is used. The operation is very simple in many applications, but can become more complex, particularly when the highest accuracy is required. It is strongly suggested that the theory and calibration techniques used be thoroughly understood to avoid possible errors.

Because different instruments measure under different conditions and have different accuracy specifications, specific instructions are given in Section 3 for each GR/IET instrument that measures high-valued capacitance. These refer to the following paragraphs which describe procedures and precautions that should be used to obtain accurate calibrations for the 1417, so that it may be used to make accurate calibrations of measuring instruments.

2.2 Connections to Instruments

2.2.1 Four-Terminal Connections

The connections to 4-terminal measuring instruments are made to the H and L CURRENT terminals and H aid L POTENTIAL terminals. Usually the terminals of the measuring instrument use a similar notation (although + may be used instead of H and - instead of L) and, in general, these terminals are connected to the corresponding terminals of the 1417, but not always. Section 3, which describes the calibration of specific GR/IET instruments, gives the proper connections for each instrument.

The 1417 also has terminals that tie directly to its case. It is not usually necessary to make any connection to this terminal. However, if strong fields are present, grounding the case may give better results. There is no direct connection between the case and the internal circuitry.

2.2.2 Terminal Impedances

The 1417 has impedance in series with each of its terminals as shown in Figure 4-4 and Table 4-2. These have no effect on a perfect 4-terminal measurement. However, no instrument makes such a perfect measurement, so that connections should be made in such a way that the larger terminal impedances are connected to the bridge or meter terminals that are most immune to such errors. This is why the various instruments described in Section 3 have different connections. There is a best way to connect to each unit.

2.2.3 Connecting Leads

The connecting leads used should be short and use reasonably heavy wire to avoid increasing these terminal impedances. However, in many cases test lead sets are supplied for particular instruments. If these lead sets are used also in the application of the instrument, it is desirable to use them for the calibration as well, so that the calibration is made on the instrument as it will be used. If special lead sets are used it must be remembered that they can affect the calibration.

2.2.4 Mutual Inductance In Leads

While a good four-terminal measurement can be immune to self-inductance in the leads, mutual inductance between the current leads and the potential leads can still cause an error, an error which is very important at very high capacitance. The equivalent circuit of figure 4.3 gives the formula

\[ C_{\text{meas}} = \frac{C}{1 + \omega^2 CM} \]

The mutual inductance can be positive or negative. This error is critical at very high capacitance, even at 100 Hz, and much more critical at 1 kHz. For example 2.5 nH gives 0.1% error when measuring 1 F at 100 Hz.
Hz or when measuring 10 mF at 1 kHz. The inductance of 2 coaxial, circular, single-turn coils both of 1-cm radius, 1 cm apart, is about 5 nH; it is obvious that great care must be taken when $\omega C$ is large.

Mutual inductance between the current circuit and the potential circuit occurs in the 1417, the connecting leads, and inside the measuring instrument. The mutual inductance inside the 1417 is usually negligible compared to that of the leads. At the highest capacitance values (100 mF and 1 F) it is approximately 1 nH. At lower values it causes negligible error at 120 Hz and less than .02% at 1 kHz.

The sections following, on the calibration of specific instruments, note the effect, if any, of the mutual inducances of these instruments and their lead sets.

### 2.3 Accuracy

#### 2.3.1 Accuracy of the 1417

With no calibration, the accuracy of the 1417 depends on many factors: the accuracy of the initial adjustment, drift in the standard capacitor, its temperature coefficient, the signal level applied, and mutual inductance in its connections. Under reasonable conditions (see pars 2.3.3), its uncalibrated or direct-reading accuracy is 0.25%.

The ultimate corrected accuracy is much better. Measurements have been made to ±0.01%. This corrected accuracy is possible because the inductive voltage dividers used to scale the capacitance values (see Section 4) are extremely precise.

This scaling accuracy is the RATIO ACCURACY given in the specifications. This is the accuracy of the ratio between the capacitance value at any setting to the value at 1 μF, assuming that the signal level on the internal standard remains constant, or that the change caused by a changing signal level is negligible, or can be precisely determined. To utilize the good ratio accuracy, it is necessary to know the 1 μF value precisely.

A changing signal level causes a change in the effective value of the standard because of the non-linearity of the input impedance of the voltage dividers (not a non-linearity in their ratios). The effect is largest at low frequencies and negligible at 1 kHz. Typical changes with level are shown in figure 2-2. Methods of determining this level change and making a suitable correction are discussed in paragraph 2.4.2 and 2.4.3.

At 1 kHz, the level effect is negligible (less than 0.004%), but (see paragraph 2.2.4) mutual inductance effects become noticeable. The RATIO ACCURACY given in the specifications for 1 kHz include the errors caused by mutual inductance inside the 1417, which are generally negligible compared to that of the leads or the measuring device.

#### 2.3.2 Required Accuracy

In calibration, it is always desirable to have the calibration accuracy of the standard far better than that of the instrument being calibrated. This is not always possible and is never possible at the highest accuracy level for any type of measurement. As a result, the accuracy of the measuring instrument may depend on the accuracy of the standard used to check it.

Suppose, for example, one has a standard whose calibrated value is known (or can be assumed to be) within ±A% and a measuring instrument whose accuracy specification is ±B%. Now we can say:

1. The instrument is within calibration if it reads the calibrated value of the standard to ±(B-A)% if $B > A$, and
2. The instrument may be within calibration if it reads the calibrated value of the standard to ±(A+B)%. There is an area of uncertainty of ±A% at both + and —tolerance limits of the instrument. if $A>B$, it is impossible to say whether the instrument is within tolerance or not.

Fortunately, most manufacturers give conservative tolerances to allow for their calibration uncertainties and for aging and environmental effects, so that measurements made in a good environment generally will be within the B-A tolerance, if the instrument is working properly, but only so long as A is not too large. If the instrument is not within the tolerance, but is within the A+B tolerance, it can be readjusted, or if this is not practical, it should be assigned a new specification of ±(A+B)%.

In many cases, the accuracy of the instrument is far
better than required by a specific application, in which case this broader A+B tolerance is of no consequence. For example, if a 0.1% instrument is used to measure ±10% capacitors, errors of a few hundredths percent in the calibration of the 1417 standard are of no importance. It may even make sense to use the simpler calibration procedures that give reduced accuracy.

2.3.3 Direct-Reading Measurements
The 1417 should be accurate to ±0.25% without corrections if:
1. Proper 4-terminal connections are made (see 2.2.1 and 2.2.2).
2. The TEST FREQUENCY switch is set to a setting (100 Hz, 120 Hz or 1 kHz) corresponding to the frequency of the test signal,
3. The ambient temperature is within the range 23°C ± 5°C and
4. At 100 Hz or 120 Hz, the level at the VOLTMETER terminals is less than 5 V (or the voltage at the CURRENT terminals is less than 0.25% of the value of Emax given in the specifications) and the dividers used in the 1417 have not been left magnetized (see paragraph 5.3) or
5. A 1-kHz test frequency is used and the voltage on the VOLTMETER terminal is 20 V or less (or the voltage at the CURRENT input terminals is less than Emax).

If these conditions are not all met, the appropriate calibration procedures given below should be used to ensure better accuracy. It is good practice to at least make the 1-pF calibration checks to avoid errors due to long-term drift.

A correction obtained from the 1 μF calibrating measurement can be used for all values and should give a measurement accuracy equal to the specified RATIO ACCURACY. The level of E_V will not affect the 1-kHz values of the 1417 so long as it is below 20 V.

![Figure 2-1. Methods of connection to binding-post](image1.png)

![Figure 2-2. Changes in capacitance vs voltage at VOLTMETER terminals.](image2.png)
2.4.2 Approximate Level Corrections

If at low frequencies (100 Hz or 120 Hz) the voltage level at the VOLTMETER terminal, \( E_v \), is not very low or is not constant at various 1417 capacitance settings, a level correction should be made for best accuracy. The method for making these corrections is given in Paragraph 2.4.3. However, for those GR/IET instruments discussed in Section 3, an approximate correction, accurate to 0.1%, can be made without making additional calibrating measurements on the 1417.

The procedure is as follows:

a. Measure the low-level 1-μF value of the 1417 at the desired test frequency as in pars 2.4.1 and note the percent deviation from the nominal value.

b. From the tables of Section 3, note the value of \( E_v \) for each specific range of the instrument used and 1417 setting. Then, from the typical calibration curve of Figure 2-2, determine the approximate percent capacitance change.

c. Apply the corrections of a and b above to the measurements made on the higher 1417 settings.

Better accuracy can be obtained if a capacitance vs level curve is made for the specific unit used.

2.4.3 The Capacitance vs Level Corrections

To obtain the best accuracy at low frequencies, it is necessary to make a precise capacitance-vs-level cor-
rection chart for the specific 1417 used, to measure the voltage at the VOLTMETER terminals at each setting of the 1417 during use, and to apply the previously determined correction. The curves should be similar to the typical correction curves of Figure 2-2. (This is not necessary at 1 kHz. It is preferable to demagnetize the dividers of the 1417 before these capacitance-vs-level corrections are determined (see Paragraph 5.3).

A precision bridge is not necessary to make this correction curve, but the bridge should have good resolution and must be able to apply 20 V to the 1 μF value or at least as much voltage as will be required in use. The bridge may be 2-terminal because the connection of the 1417 is to the VOLTMETER terminals.

The GR/IET 1615 Precision Capacitance Bridge is not suitable for these calibration measurements at low frequencies because in its normal connection it applies very little voltage to a 1 μF capacitor and in its reverse connection the detector mismatch results in poor sensitivity.

The GR/IET 1656 Impedance Bridge may be used, and, with a GR/IET 1311 oscillator, allows measurements to 20 V at 100 and 120 Hz. To obtain precision at low-levels, the recommended connection of Figure 2-3 should be used, the operator, bench, and all nearby equipment should be grounded, and the operator should not touch the metallic surfaces of the 1656 during balance. While this connection leaves the case of the 1656 ungrounded, the 1656 is battery powered, so that hum from external sources only can affect the measurement. This connection grounds the 1656 HIGH UNKNOWN terminal and the L VOLTMETER terminal of the 1417, so that a voltmeter with a grounded low terminal can be used. The voltmeter should be disconnected from the circuit for the final balance, but the voltage should be measured when the 1656 is fairly well balanced, because the voltage on the 1417 depends on bridge adjustments. With this method, resolution to 0.01% down to 0.2 V, is possible if care is used. The 1656 should be set to the 100 nF MULTIPLIER setting, so that readings will be made at full scale to obtain 0.01% readout resolution. All readings should be made using a 9 as the first (left most) digit of the readout. This allows balance up to 9XXX = 10110, which is more than adequate and minimizes the effect of errors in the main bridge adjustment.

Since the 1656 is only 0.1% accurate and because the connection may introduce a small, but constant error, the resulting calibration curve is only precise for relative values. The precise value should be obtained at a specific level (preferably a low level) by calibrating the 1656 in this connection with a precision standard or by a measurement on a precision bridge, as described in paragraph 2.4.1.

2.5 Precautions

2.5.1 Magnetization of the Divider Core

Do not apply dc to the 1417.

DC applied to the 1417 can make a severe permanent change in the inductance of the transformer dividers, which can affect the low-frequency calibrated value. A noticeable change can be made by as little as 0.1 ampere-turn and, since there are 4000 turns on one divider, it could be affected by as little as 25 μA. The result is a reduction in the inductance of the cores and a reduction in the effective value of the 1417. This effect is a result of residual magnetization of the cores. It can be removed by cyclic demagnetization, see paragraph 5.3.

A residual magnetization can also result from switching large AC signals on and off. If the signal level, EV, is above 10 V at 100 or 120 Hz, it is preferable to reduce the drive signal slowly before connecting or disconnecting the 1417.

2.5.2 Maximum Input Voltage

The maximum input voltage is given in the specifications. This limit is based on a maximum power of 2 W, a current of 2 A and a voltage of 20 V, whichever is limiting, at 100 Hz. These limits are adequate for testing all GR/IET instruments.

Aside from possible overheating, the reason for these limits is the two 1.5-W Zener diodes (in series opposing) that shunt the VOLTMETER terminals. These clamp the output to a safe value to protect the operator.
and the instrument. If the voltage at these terminals exceeds about 27 V rms, these diodes will clip and cause measurement errors. A maximum of 20 Vrms is recommended. If the 27-V limit is exceeded and the source can deliver high power, the protecting Zener diodes may be destroyed, either shorting, making the 1417 unusable, or opening, and removing the protection feature. Usually they will short unless extreme power is applied.

2.6 Use of an External Standard

An external standard may be used to get referenced values between those decade values provided or to extend the range. This can be accomplished in two ways: by using the external standard by itself or by adding it to the internal 1-μF standard.

When an external standard is connected across the EXTERNAL STANDARD terminals and the TEST FREQUENCY switch is set to that position, the 4-terminal capacitance is ideally that indicated on the CAPACITANCE switch multiplied by Cext/1 μF. This value is subject to the errors discussed in paragraph 4.3.

The low frequency error of the effective value of the EXT CAP is

$$C'_{ext} = C_{ext} \left(1 - \frac{1}{\omega^2 L C_{ext}}\right)$$

The larger the external capacitance, the less (percent) error will occur. This error can be quite large at low frequencies if Cext is substantially less than 1 μF. This error can be corrected for by use of the procedures of paragraph 2.4. If Cext is low, these corrections will be critical.

When using an external capacitor connected this way, the D value will be the D of the external capacitor plus a small quantity. The D should be determined by a measurement at the 1-μF position and it may be set to a desired value by adding resistance in series with the external standard.

When an external standard is connected to the VOLT-METER terminals, it is added in parallel with the internal standard and thus increases all values. Ideally, the measured capacitance is the value indicated by the CAPACITANCE switch multiplied by (1 + Cext/1 μF). This connection has one advantage in that the internal padding capacitors used to compensate for the low-frequency inductance error (paragraph 4.3) are still effective (if the proper TEST FREQUENCY position is used). The effective value is

$$C' = 1 \mu F + C_{ext} + C_p - \frac{1}{\omega^2 L}$$

where C_p is the padding capacitor that cancels the inductance term. Moreover, the larger Cext is, the less the (percent) error caused by this term.
However, for precise measurements this error should be determined as before paragraph 2.4. The D values will be approximately 

\[(0.01) \mu F/(\mu F + C_{ext})\].

For precise D calibrations, this value should also be determined by a measurement at the 1-\(\mu\)F setting.

### 2.7 Application of Other Frequencies

The value of the GR/IET 1417 depends somewhat on frequency (see Figure 2-4). It is padded to give good direct-reading accuracy at 100 Hz, 120 Hz and 1 kHz, but may be used at other frequencies. For frequencies between 100 Hz and 1 kHz, the full accuracy of the 1417 may be realized by measuring the 1-\(\mu\)F value of the 1417 at the desired frequency in the 1-kHz position of the TEST FREQUENCY switch and either noting the error for future use or increasing the value to 1 \(\mu\)F, by adding padding capacitors at the VOLTMETER terminals.

Below 100 Hz, the low-frequency error (see paragraph 4.3) increases and, while corrections or padding should be used, level effects will be increased so that full corrections or padding should be used, level effects will be increased so that full corrected accuracy becomes more difficult to maintain.

Above 1 kHz, the low-frequency error is negligible but the effect of mutual inductance (paragraph 4.3) is increased and eventually the divider ratios deteriorate. The increased error (both ratio and direct reading) should be less than \(\pm(0.02\%) (f/kHz)^2\), if the mutual inductance external to the 1417 is negligible. The 1-kHz TEST FREQUENCY position should be used.

At frequencies other than 100, 120 and 1000 Hz the D readings will not be 0.01. They will be approximately \((0.01)f/f_s\), where \(f\) is the test frequency and \(f_s\) the setting of the TEST FREQUENCY switch. For accurate D values, an external series capacitor and resistor should be used which, on the 1\(\mu\)F position, give a calibrated value of 1\(\mu\)F and a D of 0.01.

### 2.8 Two-Terminal Measurements

The lower values of the 1417 may be used as a 2-terminal standard at low frequencies. The D is increased because of the terminal resistances, and the C value is changed slightly because of terminal inductance and the variation in the \(\alpha\) and \(\beta\) ratios (effective terminal capacitance see paragraph 4.4). All those effects are reduced if the CURRENT and POTENTIAL terminals are connected in parallel. Table 2-1 shows approximate D values and the change in capacitance from its 4-terminal value for both this parallel connection and for connections to the current terminals only at 120 Hz.

![Figure 2-4. Typical capacitance with frequency. Note corrections are made at 100 Hz and 120 Hz.](image)

<table>
<thead>
<tr>
<th>Setting</th>
<th>(\Delta C(%))</th>
<th>D</th>
<th>(\Delta C(%))</th>
<th>D</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 (\mu)F</td>
<td>0</td>
<td>.01</td>
<td>0</td>
<td>.01</td>
</tr>
<tr>
<td>10 (\mu)F</td>
<td>+0.3</td>
<td>.063</td>
<td>+0.2</td>
<td>.045</td>
</tr>
<tr>
<td>100 (\mu)F</td>
<td>+0.8</td>
<td>.24</td>
<td>+0.4</td>
<td>.17</td>
</tr>
<tr>
<td>1 mF</td>
<td>-1.4</td>
<td>.85</td>
<td>-0.15</td>
<td>.56</td>
</tr>
<tr>
<td>10 mF</td>
<td>+2.5</td>
<td>2.85</td>
<td>+1.5</td>
<td>1.92</td>
</tr>
</tbody>
</table>

*Typical C and D values.
Section 3

USE WITH GenRad/IET BRIDGES

3.1 Use with 1617 Bridge

The 1617 Capacitance Bridge is a 1% manual unit whose accuracy depends on its dial calibration (an internal adjustment), its standard capacitor, and its range resistors. The dial calibration and standard can be checked with a precision capacitance decade box (see 1617 Instruction Manual), but a standard of high capacitance, such as the 1417, is required to check the higher range resistors. The 1617 dial is logarithmic and equally accurate at readings of 1 or 10. Therefore, the ranges can be checked at either point, or both.

Because the 1617 accuracy is only 1% and the applied power is low (if the recommended test voltages are used), the 0.25% direct-reading accuracy of the 1417 is usually adequate, but a single calibration of the 1417 at 1 μF (see para 2.4.1) is nevertheless suggested as a precaution. The voltage, $E_v$, reaches 3 V on two measurements, which would mean a capacitance change of ±0.1%, but this should be negligible compared to the ability to read the 1617 dial.

The 1617 makes a 4-terminal measurement, but errors occur if the lead impedances or terminal impedances of the 1417 are sufficiently high. To keep these errors at a minimum, different connections are used on different ranges (see Tables 3-1 and 3-2). The remaining lead errors affect $D$ on some ranges, giving a repeatable error on two lower ranges and a less precise error in three higher ranges. To check the $D$ accuracy on the higher ranges, measurements should be made at 10 on the dial.

Table 3-1 also lists the recommended 1617 test voltages (GEN LEVEL switch). When measuring high capacitance, the 1617 is subject to error caused by hum pickup. This error can and should be removed by making two measurements with the METER switch set to GEN NORM and GEN REV and taking the average readings.

The 4-terminal lead set (P/N 1617-2210) should be used (with the shorting links on the 1617 unknown

---

<table>
<thead>
<tr>
<th>1417 Setting</th>
<th>1617 Multiplier</th>
<th>Connection</th>
<th>Gen Level(V)</th>
<th>Nom C Read.</th>
<th>C Tol.</th>
<th>D Nominal</th>
<th>D Tol.</th>
<th>$E_v$ (120 Hz)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 μF</td>
<td>100 nF</td>
<td>A or B</td>
<td>2.0</td>
<td>10</td>
<td>±1%</td>
<td>.01</td>
<td>±.001</td>
<td>0.24</td>
</tr>
<tr>
<td>1 μF</td>
<td>1 μF</td>
<td>A or B</td>
<td>2.0</td>
<td>1</td>
<td>±1%</td>
<td>.01</td>
<td>±.001</td>
<td>1.45</td>
</tr>
<tr>
<td>10 μF</td>
<td>10 μF</td>
<td>A</td>
<td>0.5</td>
<td>1</td>
<td>±1%</td>
<td>.008</td>
<td>±.001</td>
<td>0.77</td>
</tr>
<tr>
<td>10 μF</td>
<td>10 μF</td>
<td>A</td>
<td>0.5</td>
<td>10</td>
<td>±1%</td>
<td>.009</td>
<td>±.001</td>
<td>0.64</td>
</tr>
<tr>
<td>100 μF</td>
<td>100 μF</td>
<td>A</td>
<td>0.2</td>
<td>1</td>
<td>±1%</td>
<td>.009</td>
<td>±.001</td>
<td>1.4</td>
</tr>
<tr>
<td>1 mF</td>
<td>100 μF</td>
<td>B</td>
<td>0.2</td>
<td>10</td>
<td>±1%</td>
<td>.01</td>
<td>±.001</td>
<td>0.75</td>
</tr>
<tr>
<td>1 mF</td>
<td>.1 mF</td>
<td>B</td>
<td>0.2</td>
<td>1</td>
<td>±1%</td>
<td>--</td>
<td></td>
<td>3.0</td>
</tr>
<tr>
<td>10 mF</td>
<td>1 mF</td>
<td>B</td>
<td>0.2</td>
<td>10</td>
<td>±1%</td>
<td>.01</td>
<td>±.0011</td>
<td>1.55</td>
</tr>
<tr>
<td>10 mF</td>
<td>10 mF</td>
<td>B</td>
<td>0.2</td>
<td>1</td>
<td>±1%</td>
<td>--</td>
<td></td>
<td>3.1</td>
</tr>
<tr>
<td>100 mF</td>
<td>10 mF</td>
<td>B</td>
<td>0.2</td>
<td>10</td>
<td>±1%</td>
<td>.01</td>
<td>±.002</td>
<td>1.50</td>
</tr>
<tr>
<td>100 mF</td>
<td>100 mF</td>
<td>B</td>
<td>0.2</td>
<td>1</td>
<td>±2%</td>
<td>--</td>
<td></td>
<td>1.55</td>
</tr>
<tr>
<td>1 F</td>
<td>100 mF</td>
<td>B</td>
<td>0.5</td>
<td>10</td>
<td>±2%</td>
<td>.01</td>
<td>±.011</td>
<td>1.0</td>
</tr>
</tbody>
</table>

NOTES: (1) Use 1417 frequency setting corresponding to test frequency.
(2) Make two measurements with 1617 input direct and reversed and take average.
(3) Twist leads at C values >1 mF.
3.3 Use with 1683 Bridge

The GR/IET 1683 Automatic R LC Bridge measures at both 120 Hz and 1 kHz to a base accuracy of 0.1% (see Table 3-5) so that calibration to full accuracy requires careful calibration and use of the 1417.

CAUTION

GR 1683 applies 2.2 V at 1 A; danger of residual magnetization (see Para 2.5.1.)

At 120 Hz, a voltage-vs-level curve should be used to obtain full accuracy (see Para 2.4.2). At 1 mF full output of the 1683 can give 25.5 V on the 1417 VOLTMETER terminals, which is close to the Zener-diode clipping point. It is suggested that the GR/IET 1683 OSC LEVEL be set to 1 V for this measurement. Because of the high level supplied by this bridge, it is recommended that the 1417 be connected and disconnected and all range changes made with the 1683 OSC LEVEL set to 0.

At 1 kHz there is negligible level error but the above precaution is recommended to avoid changes in the 1417 calibration due to transients.

The measurement connections between the 1683 and 1417 are given in Table 3-7. The mutual inductance of the 1683 gives negligible error compared to its specifications, if the current leads are twisted together.

3.2 Use With 1657 Digibridge

The accuracy of the 1657 depends mainly on three precision resistors and a crystal oscillator. These can most easily be checked by resistance measurements of 10 Ω, 1 kΩ and 100 kΩ, and one precise capacitance measurement. However, if one wants assurance that it measures high capacitance values accurately also, one may use the 1417.

The 1657 is 0.2% accurate at the lower values, so that the 1417 should be calibrated before it is used. However, because the 1657 applies low power (see Table 3-3) a single, low-level calibration (see para 2.4.1) for each frequency is all that is required.

Connections between the 1657 and the 1417 should be made as given in Table 3-4. The 1657 I+ connection may have a slight DC offset voltage. A large capacitor (500 μF) should be put in series with the connection.

A test lead set should be used. The mutual inductance of the 1657 gives negligible error compared to its specifications, but care should be used in the lead configuration when measuring 1 mF at 1 kHz.

Note that the maximum reading is 999.99 mF at 1 kHz and 99.999 mF at 120 Hz. It would be preferable to use an external standard with the 1417, of say 0.9 μF to check these range extremes.
### Table 3-5

<table>
<thead>
<tr>
<th>Setting</th>
<th>120 Hz ΔC(%)</th>
<th>D</th>
<th>1 kHz ΔC(%)</th>
<th>D</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 µF</td>
<td>0.01</td>
<td>.0011</td>
<td>0.11</td>
<td>.0011</td>
</tr>
<tr>
<td>10 µF</td>
<td>0.01</td>
<td>.0011</td>
<td>0.11</td>
<td>.0011</td>
</tr>
<tr>
<td>100 µF</td>
<td>0.01</td>
<td>.0011</td>
<td>0.11</td>
<td>.0011</td>
</tr>
<tr>
<td>1 mF</td>
<td>0.11</td>
<td>.0011</td>
<td>1.1</td>
<td>.01</td>
</tr>
<tr>
<td>10 mF</td>
<td>1.1</td>
<td>.01</td>
<td>6</td>
<td>0.1</td>
</tr>
<tr>
<td>100 mF</td>
<td>6</td>
<td>0.1</td>
<td>—</td>
<td>—</td>
</tr>
</tbody>
</table>

*1683 C reading of 10000 and D = .01

### Table 3-6

<table>
<thead>
<tr>
<th>Setting</th>
<th>Full 1 V</th>
<th>1 V</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 µF</td>
<td>2.1</td>
<td>1</td>
</tr>
<tr>
<td>10 µF</td>
<td>5.3</td>
<td>2.5</td>
</tr>
<tr>
<td>100 µF</td>
<td>14.7</td>
<td>6.9</td>
</tr>
<tr>
<td>1 mF</td>
<td>25.5</td>
<td>12</td>
</tr>
<tr>
<td>10 mF</td>
<td>17.8</td>
<td>8.4</td>
</tr>
<tr>
<td>100 mF</td>
<td>7.6</td>
<td>3.6</td>
</tr>
</tbody>
</table>

### Table 3-7

<table>
<thead>
<tr>
<th>1417</th>
<th>1683</th>
<th>CURRENT</th>
<th>CURRENT H</th>
<th>CURRENT L</th>
<th>CURRENT L</th>
</tr>
</thead>
<tbody>
<tr>
<td>+CURRENT</td>
<td>+POTENTIAL</td>
<td>-CURRENT</td>
<td>-POTENTIAL</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

### Table 3-8

<table>
<thead>
<tr>
<th>Setting</th>
<th>120 Hz</th>
<th>1 kHz</th>
<th>1686 Ev Approx</th>
<th>1685 Ev Approx</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 µF</td>
<td>±0.35</td>
<td>±0.019</td>
<td>±0.12</td>
<td>±0.0019</td>
</tr>
<tr>
<td>10 µF</td>
<td>±0.35</td>
<td>±0.019</td>
<td>±0.12</td>
<td>±0.0019</td>
</tr>
<tr>
<td>100 µF</td>
<td>±0.35</td>
<td>±0.019</td>
<td>±0.36</td>
<td>±0.0057</td>
</tr>
<tr>
<td>1 mF</td>
<td>±0.7</td>
<td>±0.0038</td>
<td>±1.2**</td>
<td>±0.019</td>
</tr>
<tr>
<td>10 mF</td>
<td>±1.4</td>
<td>±0.0176</td>
<td></td>
<td>±0.09</td>
</tr>
<tr>
<td>100 mF</td>
<td>6±</td>
<td></td>
<td>±0.4</td>
<td>±0.4</td>
</tr>
</tbody>
</table>

*Overrange.
*At half full scale and D = .01.
**Range not recommended.

### Table 3-9

<table>
<thead>
<tr>
<th>Setting</th>
<th>120 Hz</th>
<th>1 kHz</th>
<th>1686 Ev</th>
<th>1685 Ev (120 Hz)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 µF</td>
<td>±0.35</td>
<td>±0.0019</td>
<td>±0.12</td>
<td>±0.0019</td>
</tr>
<tr>
<td>10 µF</td>
<td>±0.35</td>
<td>±0.0019</td>
<td>±0.12</td>
<td>±0.0019</td>
</tr>
<tr>
<td>100 µF</td>
<td>±0.35</td>
<td>±0.0019</td>
<td>±0.36</td>
<td>±0.0057</td>
</tr>
<tr>
<td>1 mF</td>
<td>±0.7</td>
<td>±0.0038</td>
<td>±1.2</td>
<td>±0.019</td>
</tr>
<tr>
<td>10 mF</td>
<td>±1.06</td>
<td>±0.0176</td>
<td></td>
<td>±0.9</td>
</tr>
<tr>
<td>100 mF</td>
<td>±3.4</td>
<td>±0.11</td>
<td></td>
<td>±0.4</td>
</tr>
<tr>
<td>1 F</td>
<td>28±</td>
<td></td>
<td></td>
<td>±0.1</td>
</tr>
</tbody>
</table>

*Overrange
*At half full scale and D = .01.
3.6 Use with IET Digibridges 1689/1693 and Other LCR Meters including 7600 Plus and 1920.

3.6.1 General
The 1417 Four-Terminal Capacitance Standard is useful for checking the accuracy of the QuadTech low frequency Digibridges at high capacitances. These instruments are calibrated using precision resistors, but the 1417 can give assurance that they measure high capacitance accurately. These instruments have different specified accuracy and different capacitance ranges so that detailed information is given for each in the paragraphs that follow. All of them have accuracy better than the 0.25 % direct-reading accuracy of the 1417 and therefore 1 μF calibrations of the 1417 are required and some care necessary to realize its high ratio accuracy (refer to paragraphs 2.4.2 and 5.3). In some cases the specified Digibridge accuracy is as good as the 1417 ratio accuracy so that a true accuracy verification is not possible but nevertheless, tests with a 1417 gives added confidence.

3.6.2 Connections
Four-terminal connections should be made to the 1417 as shown in Table 3-14 below using the 1657-9600 Extender Cable (1657, 1658, 1659, 1689, 1692) or the 1689-9602 Extender Cable (1689M, 1693, 7600 Plus and 1920).

The most LCR meters may have a slight DC voltage at the current terminals, IH or I+ so that a large capacitor, > 200 μF, should be connected in series with the H CURRENT terminal to keep dc current from flowing in the precision transformers (this may be an electrolytic capacitor). If dc current flows there will be an error in the capacitance measurement so use of this blocking capacitor is always recommended.

---

**Table 3-14**

Digibridge Test Connections

<table>
<thead>
<tr>
<th>1417 Terminal</th>
<th>Cable Plug Color Code</th>
<th>Digibridge Connection</th>
<th>Test Fixture Connection</th>
</tr>
</thead>
<tbody>
<tr>
<td>H POTENTIAL</td>
<td>Red-white</td>
<td>P+</td>
<td>right rear</td>
</tr>
<tr>
<td>L POTENTIAL</td>
<td>Black-White</td>
<td>P-</td>
<td>left rear</td>
</tr>
<tr>
<td>H CURRENT*</td>
<td>Red-Red</td>
<td>I+</td>
<td>right front</td>
</tr>
<tr>
<td>L CURRENT</td>
<td>Black-Black</td>
<td>I-</td>
<td>left front</td>
</tr>
<tr>
<td>CASE</td>
<td>Green</td>
<td>Ground</td>
<td>threaded holes</td>
</tr>
</tbody>
</table>

*Add series capacitor*
3.6.3 Zeroing and Minimizing Mutual Inductance

Mutual inductance between the test leads can cause an error, particularly at 1 kHz (refer to paragraph 2.2.4), even though the position of the terminals on the 1417 minimizes this effect. It is also suggested that the free ends of the test cable be twisted to avoid coupling between the current and potential circuits. Twist the two POTENTIAL leads together and twist the two CURRENT leads together. Many instruments have short-circuit calibration capability that should be used to remove this effect. The suggested procedure is as follows:

a. Set the Digibridge to the test frequency to be used.
b. Connect the 1417 as in Table 3-14 except plug the high potential plug (red-white) into the low potential plug (black-white) which is plugged in the L POTENTIAL terminal of the 1417.
c. Set the 1417 CAPACITANCE switch to 1F.
d. Make the SHORT calibration of the test instrument.
e. Reconnect the high potential plug into the H POTENTIAL terminal being careful to move the leads as little as possible.
f. Reset to desired capacitance value.

3.6.4 Use with the IET 1657 or 1658 Digibridges

These two instruments both have three ranges which are usually checked by three calibrated resistors (10 ohms, 1 kohms, and 100 kohms). Their capacitance ranges extend to 99999 μF at 100 or 120 Hz and 999.99 μF at 1 kHz. This means that 100 mF at 120 Hz and 1000 μF at 1 kHz may only be measured if they are lower than the nominal value. A chart of the C and D accuracy of the 1657 vs capacitance is given in Table 3-15. Divide the accuracy values by two for the 1658 using its SLOW measurement rate.

Both of these instruments can apply enough current to give a value for $E_v$ of over 1 V at 100 μF (refer to Figure 2-2) but both instruments should still measure well within their accuracy (relative to the 1 μF calibrated value at each frequency).

Neither of these instruments makes a SHORT calibration so that care should be used to keep the mutual inductance at a minimum (refer to paragraph 3.1.2). The 1659 and 1692 Digibridges have direct reading ranges well beyond the range of the 1417 standard but their specified accuracy gets very high above 1 F so that there is no point in checking accuracy above that value. The actual accuracy will be well below the values given in Table 3-16 if a SHORT calibration is made at the test frequency and many measurements are averaged. The 1659 applies 0.3 V max but the 1692 has a choice of 0.3 V or 1 V (open circuit). The 0.3V level of the 1692 should be used up to 1 mF at 120 Hz and up to 100 μF at 1 kHz. This 0.3 V level will cause an noticeable increase in the 100 μF value at 100 or 120 Hz, about 0.03 %, which is less than the 1692 specification, but which should either be allowed for or reduced by reducing the signal level by putting about 30 ohms in series with the H CURRENT connection. These instruments have direct reading capacitance ranges to 99999 μF (just under 100 mF) but this may be extended by using the RATIO mode. To do this:

a. Go to range 4 by measuring a high C value or setting
   
   [4] [=] [SHIFT] [SPECIAL] [1]
   
   b. Enter 1000 as the nominal value
      [1000] [=] [NOM VAL]
   
   c. Set RATIO mode
      [2] [=] [SHIFT] [SPECIAL] [6]

      The reading is now in mF (although there is no unit displayed) and the range is extended to 99999 mF or 99.999 F.

To return to the normal mode enter
   
   [0] [=] [SHIFT] [SPECIAL] [6]

These three instruments are very similar in behavior but are specified differently. They all have measurement accuracy at 1 kHz at 1 μF and 10 μF that is comparable to the 1417 ratio accuracy so that a true accuracy verification is difficult under these conditions (refer to paragraph 2.3.2), but these measurements can increase confidence. The 1417 ratio accuracy is adequate under most other conditions. At very high values the instrument specs are very broad and easily met. However, if a zero is made and many measurements averaged, the
actual Digibridge accuracy can be quite good. 1% accuracy at 1 F at 100 Hz is possible.

These instruments have such tight tolerances that the changes in the 1417 values with signal level are important. This leads to a somewhat paradoxical situation in that lowering the signal level will give better values of the 1417 standard but will result in reduced specified accuracy of the instruments. Fortunately this occurs only at 100 or 120 Hz where the spec is broader. Moreover, the accuracy at reduced levels is much better than specified, particularly if several measurements are averaged, so that the specified averaged, so that the specified accuracy at 1 V should be met and thus Table 3-17 gives these values even though it lists recommended signal levels that are substantially lower at 120 (or 100) Hz.
Table 3-17

<table>
<thead>
<tr>
<th>Setting</th>
<th>100 &amp; 120 Hz</th>
<th>1 kHz</th>
<th>100 &amp; 120 Hz</th>
<th>1 kHz</th>
<th>Test V</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>C</td>
<td>D</td>
<td>C</td>
<td>D</td>
<td>C</td>
</tr>
<tr>
<td>1 uF</td>
<td>.04%</td>
<td>.0004</td>
<td>.02%</td>
<td>.0002</td>
<td>.04%</td>
</tr>
<tr>
<td>10 uF</td>
<td>.04%</td>
<td>.0004</td>
<td>.02%</td>
<td>.0002</td>
<td>.041%</td>
</tr>
<tr>
<td>100 uF</td>
<td>.04%</td>
<td>.0004</td>
<td>.05%</td>
<td>.0005</td>
<td>.047%</td>
</tr>
<tr>
<td>1 mF</td>
<td>144%</td>
<td>.00144</td>
<td>.41%</td>
<td>.0041</td>
<td>.112%</td>
</tr>
<tr>
<td>10 mF</td>
<td>1.44%</td>
<td>.0144</td>
<td>4%</td>
<td>.04</td>
<td>.76%</td>
</tr>
<tr>
<td>100 mF**</td>
<td>14.4%</td>
<td>.144</td>
<td>-</td>
<td>-</td>
<td>7.24%</td>
</tr>
<tr>
<td>1 F**</td>
<td>144%</td>
<td>1.4</td>
<td>-</td>
<td>-</td>
<td>72%</td>
</tr>
</tbody>
</table>

* Recommended programmed voltage at 100 or 120 Hz, see text
** Use RATIO mode to extend capacitance range
Chapter 4
THEORY

4.1 General
Capacitors that are stable enough for use as standards would be extremely expensive at high capacitance values. Electrolytic types have poor stability. The 1417 is a network that simulates higher values, effectively multiplying the capacitance of its 1 μF standard, a stabilized polystyrene capacitor, to higher values uses by means of two inductive voltage dividers (IVD's). It should not be used as a circuit component, for it cannot be used at DC and, generally, it must be used as a 4-terminal device (see Paragraph 2.8).

4.2 Basic Circuit
The basic circuit of the GR 1417 is shown in Figure 4-1. C is the internal 1μF and the two autotransformers are IVD's with selectable ratios α and β. The open-circuit transfer impedance of this network Z_T = E_o / I_o. This transfer impedance is decreased by attenuating the input current by a factor α and the output voltage by β. If the dividers are ideal, that is they have exact and equal ratios, infinite open-circuit impedance and zero short circuit (winding) impedance, the equivalent for this network becomes a 2-terminal capacitor of value C/αβ, where α and β are the nominal ratios.

Needless to say, the IVD's are not ideal, and each imperfection causes specific additions to such an ideal equivalent circuit.

Figure 4-1. Basic circuit diagram.

4.3 Errors in Effective 4-Terminal Capacitance
The transformers of Figure 4-1 are passive 3-terminal networks and can be replaced by equivalent, 3-impedance T networks, as shown in Figure 4-2. It should be noted that these impedances are slightly non-linear.

For this network the transfer admittance is

\[ Y_T = \frac{1}{Z_T} = \frac{I_{in}}{E_o} = \left( \frac{Z_2 + Z_3}{Z_2} \right) \left( \frac{Z_5 + Z_6}{Z_5} \right) \times \left( j\omega C + \frac{1}{Z_2 + Z_3} + \frac{1}{Z_5 + Z_6} \right) \]

The first two factors are the reciprocals of the open-circuit divider ratios α and β in the 1417. It is relatively easy to get very precise ratios with high permeability toroidal cores. The dividers of the GR 1417 have ratio errors that are negligible compared to any of its specifications. The feature of the GR 1417 is that, if the last factor can be held constant, the ratio between capacitance settings depends only on these precise divider ratios. This is reflected in the specified fied ratio accuracy, which is the ratio between the capacitance at any setting and that at 1 μF, if conditions are such that the last factor is constant.

The last factor is the admittance of capacitor C in parallel with the open-circuit input impedances of the dividers. These divider impedances are independent of the ratio settings α and β. If we assume that these divider impedances are purely inductive and (in parallel) have an inductance L, the last factor becomes:

\[ j\omega C \left( 1 - \frac{1}{\omega^2 LC} \right) = j\omega C' \]
So that the transfer capacitance

\[ C_T = \frac{Y_T}{j\omega} = \frac{C}{\alpha\beta} \left(1 - \frac{1}{\omega^2 LC}\right) = \frac{C'}{\alpha\beta} \]

Where \( C' \) is the effective value of the standard \( C \). This value has an error that depends on frequency squared, as shown. It also depends on signal level because of the nonlinearity of inductance \( L \). To keep this error small, the LC product should be kept as large as possible.

It was considered important to have the minimum value of capacitance (\( C' \)) be 1 \( \mu F \), so that this value could be measured on precision bridges, or compared against a precision standard (see para 2.4). Available bridges and standards are substantially less accurate at higher values. This capacitance can be measured by a two-terminal bridge (A to A’ in Figure 4-2).

Therefore, to keep the LC product high, the value of \( L \) is quite high, \( \approx 400 \) H. With a 1-\( \mu F \) capacitor, this causes error of about 0.50% at 120 Hz. However, because \( L \) is reasonably constant, this error is compensated by additional padding capacitance at 100 Hz and 120 Hz, to give a direct-reading accuracy of .0.25% over a reasonable range of voltage levels. Correcting for this error can give much better accuracy (para 2.4) This error is negligible at 1 kHz.

There is an additional source of error which is important at higher frequencies. If there is mutual inductance between the input (CURRENT) circuit and the output (POTENTIAL) circuit, as shown in Figure 4-3a, it appears effectively as an inductance in series with the standard CT (Figure 4-3b). This mutual inductance can be + or — and gives an error

\[ C'_{CT} = \frac{C_T}{1+\omega^2 MC_T} \]

where \( C_T \) is the 4-terminal transfer capacitance. Usually the coupling internal to the 1417 is substantially less than the coupling in the external connections (para 2.2) but there is some. The effect of the internal coupling is included in the RATIO ACCURACY specification.

4.4 Terminal Impedances

Although a true 4-terminal measurement would be completely immune to impedance in series with any of the terminals, no instrument makes such an ideal measurement, so these impedances should be kept small to avoid measurement errors. The terminal impedances of the 1417 are shown in Figure 4-4. The lower two branch, or terminal, impedances of Figure 4-4 are small, for they are simply the impedance of
wired common connection of Figure 4-1. The upper two terminal impedances are much more critical. From the equivalent circuit of Figure 4-2, the (open-circuit) input impedance

\[
Z_{in} = Z_1 + \frac{Z_2}{Z_2 + Z_3} + \left(\frac{Z_2}{Z_2 + Z_3}\right)^2 + \frac{1}{j\omega C' + \frac{1}{Z_2 + Z_3} + \frac{1}{Z_5 + Z_6}}
\]

which can be written as:

\[
Z_{in} = Z_{isc} + \frac{\alpha^2}{j\omega C'}
\]

where \(Z_{isc}\) is the input impedance with a short across the capacitor. Subtracting the transfer impedance, we get the upper input terminal impedance

\[
Z_{in} - Z_T = Z_{isc} + \frac{\alpha^2}{j\omega C'} - \frac{\alpha \beta}{j\omega C'} = Z_{isc} + \frac{\alpha(\alpha - \beta)}{j\omega C'}
\]

(The other terminal is easily found by symmetry.)

The first term is mostly resistive and a function of the winding resistance of the input divider. For a given core and winding area, this resistance is proportional to \(N^2\), where there is a tradeoff between keeping this resistance low and keeping the error of equation 2 small. The compromise reached was based on the characteristics of several instruments. These windings also have some leakage inductance, but the impedance of this inductance is small compared to the resistance, even at 1 kHz. These impedances are given in Table 4-2.

The second term in this terminal impedance is capacitive (+ or -) and is the result of inequality between the divider ratios \(\alpha\) and \(\beta\). These ratios can be exactly equal (nominally) when their product is \(10^2\), \(10^4\), or \(10^6\), but, when it is \(10^1\), \(10^3\), or \(10^5\), they can be equal only if they are irrational numbers. If these dividers are autotransformers and possible ratios are limited to ratios of integral numbers of turns, either these \(\alpha\beta\) ratios must be slightly inaccurate or \(\alpha\) and \(\beta\) must differ. A resistive divider across one turn of one divider can, however, give greatly increased resolution. Such a divider is used on the 10-\(\mu\)F setting because this capacitive terminal impedance must be small to avoid an error on a specific bridge. Table 4-1 shows the winding scheme used and the resulting capacitive term of equation 6. Capacitance is always much less than the winding resistance, even at 50 Hz.

### 4.5 Dissipation Factor

The dissipation factor of the 1417 is intentionally set to be 0.01 at 100 Hz, 120 Hz and 1 kHz (at the appropriate switch setting). This insures that, in spite of instrument errors, the D indication will be positive, allowing a valid D reading and proper operation. This is done by adding resistors in series with the standard capacitor, as shown in Figure 5-3.

The D value is affected by loss in the IVD's and changes with level slightly, but should remain within ±0.001. The D also includes loss in the standard capacitor and phase-shift errors in the IVD’s but the effects are small.

<p>| Table 4-1 |
|---|---|---|---|---|---|---|
|</p>
<table>
<thead>
<tr>
<th>Setting</th>
<th>Current</th>
<th>(C^*)</th>
<th>Potential</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 (\mu)F</td>
<td>0.19 (\Omega)</td>
<td>3 (\mu)H</td>
<td>0.024 (\mu)F</td>
</tr>
<tr>
<td>10 (\mu)F</td>
<td>7.1 (\mu)H</td>
<td>567 (\mu)H</td>
<td>15 (\mu)F</td>
</tr>
<tr>
<td>100 (\mu)F</td>
<td>3.06 (\mu)H</td>
<td>123 (\mu)H</td>
<td>63 (\mu)F</td>
</tr>
<tr>
<td>1 (\mu)F</td>
<td>1.1 (\mu)H</td>
<td>19.5 (\mu)H</td>
<td>2.15 (\mu)F</td>
</tr>
<tr>
<td>10 (\mu)F</td>
<td>0.36 (\mu)H</td>
<td>3.5 (\mu)H</td>
<td>0.70 (\mu)F</td>
</tr>
<tr>
<td>100 (\mu)F</td>
<td>0.125 (\mu)H</td>
<td>1.0 (\mu)H</td>
<td>0.205 (\mu)F</td>
</tr>
<tr>
<td>1 (\mu)F</td>
<td>0.035 (\mu)H</td>
<td>3 (\mu)H</td>
<td>0.055 (\mu)F</td>
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*Calculated from ratios of Table 4-1.*
<table>
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<tr>
<th>Setting</th>
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<th>Current L</th>
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<th>Potential L</th>
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</thead>
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<td>.3 µH</td>
<td>.024 Ω</td>
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<td>.205 Ω</td>
<td>1.2</td>
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<tr>
<td>1 F</td>
<td>.035 Ω</td>
<td>.3 µH</td>
<td>.055 Ω</td>
<td>.4 µH</td>
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</tbody>
</table>

*Calculated from ratios of Table 4-1.

Table 4-2
Chapter 5

SERVICE AND MAINTENANCE

5.1 IET Field Service
Our warranty attests the quality of materials and workmanship in our products. When difficulties do occur, our service engineers will assist in any way possible. If the difficulty cannot be eliminated by use of the following service instructions, please write or phone the nearest IET service facility, giving full information of the trouble and of steps taken to remedy it. Describe the instrument by type, serial, and ID numbers (Refer to front and rear panels.)

5.2 Instrument Return
Before returning an instrument for service, please ask our nearest office for a "Returned Material" number. Use of this number in correspondence and on a tag tied to the instrument will ensure proper handling and identification.

For return shipment, please use packaging that is adequate to protect the instrument from damage, i.e., equivalent to the original packaging. Advice may be obtained from any IET office.

5.3 Calibration Procedure
If the low-level value of the 1 μF CAPACITANCE setting for any TEST FREQUENCY position is found to be outside limits at 23° ±5°C, (see section 2) its padding capacitors may be added or removed to bring it in tolerance. However, before repadding, cyclically demagnetize the divider cores as described in para 5.5, to ensure that the error is not caused by residual magnetization.

The padding capacitors for the 1-kHz position are on the capacitance standard C. This adjustment affects all TEST FREQUENCY switch positions and should be adjusted first. The 100-Hz and 120-Hz positions have separate padding capacitors and these padding capacitors are located on the TEST FREQUENCY switch, see Figure 5-1.

If the 1-μF capacitance measurement indicates a short circuit, or if the waveform at the VOLTMETER terminals is non-sinusoidal (when a low-level sinusoidal signal is applied), the Zener diodes CR1 and CR2 may be shorted and should be replaced.

The other values of capacitance for the other CAPACITANCE settings are related to this 1-μF value by the process ratios of the inductive dividers Ti and
Except for gross damage or burn out, these ratios should not change with time.

5.4 Determination of Other Parameters

5.4.1 Divider Ratios

The divider ratios for both $T_1$ and $T_2$ can be checked by comparing them with a precision inductive voltage divider, using standard ratio measuring techniques. The input voltage (<20V) should be applied at the VOLTTERMETER terminals and the outputs checked at both the CURRENT terminals and the outputs checked at both the CURRENT terminals and POTENTIAL terminals. The TEST FREQUENCY switch should be set to the EXTERNAL STANDARD position.

The ratios should be those values given in Table 4-1 to within ± 0.005%, from 50 Hz to 2 kHz. A high-resolution divider is required to measure the 1000 to 1 ratio to this precision. One should note that the 1417 could have applications as a precision divider.

5.4.2 Terminal Impedances

The resistive and inductive parts of the terminal impedances can be measured with a suitable bridge. The GR 1608 is recommended because it can measure $R$ and $Q$ ($L = RQ/\omega$). The measurements are made at the CURRENT terminals to measure $Z_A$ (see specs) and at the POTENTIAL terminals to measure $Z_B$. The VOLTTERMETER terminals should be shorted. The parameters $L$ and $R$ vary only slightly with frequency.

The capacitive part of the terminal impedances is a function of the $a$ and $b$ ratios and can be determined by calculation. (See Section 4).

5.4.3 Divider Inductance

The open-circuit inductance for the two dividers in parallel ($L$ in equations of section 4) can be determined by measuring inductance across the VOLTTERMETER terminals with the TEST FREQUENCY switch in the EXTERNAL STANDARD position. A low-frequency signal should be used because the dividers are resonant at 1 kHz (appear capacitive). The inductance should be approximately 400 H at 100 Hz, at low-levels.

5.5 Service

5.5.1 Demagnetization of Divider Cores

Residual magnetization in the divider cores, resulting from the application of dc or a large ac transient, can be removed by cyclic demagnetization. In this procedure, the cores are cycled around their internal loops with a large signal, which is then slowly reduced in level to zero, leaving the cores unmagnetized. The amplitude of the signal should be less than 27 V or 2W, to avoid damage to the 39V, 1.5-W Zener diodes that shunt the cores. Therefore, to get a high flux level, a very low frequency signal should be used; 20 V at 20 Hz is adequate and at lower frequencies the level may be proportionally lower. An IET 1310 set for full output at 5 Hz, can provide a satisfactory signal.

Procedure:

a. Set the 1417 TEST FREQUENCY switch to EXTERNAL STANDARD.

b. With the oscillator at low level, connect it to the 1417 VOLTTERMETER terminals.

c. Increase the oscillator level (not more than 25 V, see above).

d. Slowly decrease the oscillator level to zero.

5.5.2 Knob Removal

If it should be necessary to remove the knob on a front-panel control, either to replace one that has been damaged or To replace the associated control, proceed as follows:

a. Grasp the knob firmly with the fingers and pull the knob straight away from the panel.

b. Observe the position of the set screw in the bushing, with respect to any panel marking (or at the full ccw position of a continuous control).

c. Release the set screw and pull the bushing off the shaft.

d. Remove and retain the black Nylon thrust washer, behind the dial/knob assembly, as appropriate.

NOTE

To separate the bushing from the knob, if for any reason they should be combined off the instrument, drive a machine tap a turn or two into the bushing for a sufficient grip for easy separation.
5.5.3 Knob Installation

To install a knob assembly on the control shaft:

a. Place the black Nylon thrust washer over the control shaft, if appropriate.
b. Mount the bushing on the shaft, using a small slotted piece of wrapping paper as a shim for adequate panel clearance.
c. Orient the set screw on the bushing with respect to the panel-marking index and lock the set screw.

NOTE
Make sure that the end of the shaft does not protrude through the bushing or the knob won’t set properly.

d. Place the knob on the bushing with the retention spring opposite the set screw.
e. Push the knob in until it bottoms and pull it slightly to check that the retention spring is seated in the groove in the bushing.

NOTE
If the retention spring in the knob comes loose, reinstall it in the interior notch with the small slit in the outer wall.
<table>
<thead>
<tr>
<th>Model Ref</th>
<th>IET Pt No</th>
<th>Description</th>
</tr>
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<td>Binding Post, Red</td>
</tr>
<tr>
<td>2</td>
<td>01-1033-8-0310</td>
<td>Binding Post, Black</td>
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<td>3</td>
<td>01-1008-1-0310</td>
<td>Binding Post, Gold</td>
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<td>5500-5421-01</td>
<td>Knob, IET Style</td>
</tr>
<tr>
<td>Not Shown</td>
<td>5331-3100</td>
<td>Gasket</td>
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</tbody>
</table>
Figure 5-2. TEST FREQUENCY Switch (S2) details showing padding capacitors.